AMENDMENTS TO THE CLAIMS

- 1-7. (Canceled)
- 8. (Previously Presented) A transistor device having a gate electrode overlying a gate dielectric formed directly on a semiconductor substrate, the gate dielectric comprising:

a first dielectric material selected from the group consisting of HfO₂, BaO, La₂O₃, Y₂O₃, and ZrO₂ and having a first dielectric constant; and

a second dielectric material having a second dielectric constant different from the first dielectric constant,

the first and second dielectric materials being scalable for a set of feature size technologies, the set of feature size technologies defined by a gate length in the range of 25-70 nm, wherein the first material thickness and the second material thickness are determined by the relationship

$$t_1/k_1 + t_2/k_2 = t_{ox}/k_{ox}$$

wherein

t₁ is the first material thickness,

t₂ is the second material thickness,

t_{ox} is the minimum thickness for a gate dielectric of silicon dioxide for a

chosen gate length,

k₁ is the dielectric constant for the first dielectric material,

k₂ is the dielectric constant for the second dielectric material, and

k_{ox} is the dielectric constant of silicon dioxide, and

wherein the transistor device is isolated from other devices by shallow trench structures.

- 9. (Original) The transistor of claim 8, wherein the second dielectric of the gate dielectric has a dielectric constant greater than the first dielectric constant.
- 10. (Original) The transistor of claim 8, wherein the first material of the gate dielectric has a first thickness and the second material of the gate dielectric has a second thickness, the

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combination of the first thickness and the second thickness defining a total thickness less than one-third of a length of the transistor gate.

11-12. (Canceled)

- 13. (Original) The gate dielectric of claim 8, wherein the second dielectric material is selected from one of BST and PZT.
- 14. (Original) The gate dielectric of claim 8, further comprising a third dielectric material having a third dielectric constant.
 - 15. (Previously Presented) An apparatus comprising:

a semiconductor substrate having a transistor device formed thereon, the transistor device isolated from other devices by shallow trench structures and having a gate dielectric disposed directly between a surface of the substrate and a gate electrode comprising:

a first dielectric material selected from the group consisting of HfO₂, BaO, La₂O₃, Y₂O₃, and ZrO₂ and having a first dielectric constant; and

a second dielectric material having a second dielectric constant different from the first dielectric constant,

the first and second dielectric materials being scalable for each of a plurality of feature size technologies, having a gate length in the range of 25-70 nm, and

wherein the first material thickness and the second material thickness are determined by the relationship

$$t_1/k_1 + t_2/k_2 = t_{ox}/k_{ox}$$

wherein

t₁ is the first material thickness,

t₂ is the second material thickness,

t_{ox} is the minimum thickness for a gate dielectric of silicon dioxide for a

chosen gate length,

k₁ is the dielectric constant for the first dielectric material,

k₂ is the dielectric constant for the second dielectric material, and

 k_{ox} is the dielectric constant of silicon dioxide.

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- 16. (Previously Presented) The apparatus of claim 15, wherein the second dielectric constant is greater than the first dielectric constant.
- 17. (Previously Presented) The apparatus of claim 15, wherein the first material has a first thickness and the second material has a second thickness, the combination of the first thickness and the second thickness defining a total thickness less than one-third of the length of a transistor gate adapted to overly the gate dielectric.

18-19. (Canceled)

- 20. (Previously Presented) The apparatus of claim 15, wherein the second dielectric material is selected from one of BST and PZT.
- 21. (Previously Presented) The apparatus of claim 15, further comprising a third dielectric material having a third dielectric constant.

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